

STIC Database Tracking Number: 271810

To: JOSE DEES  
Location: JEF-8D59  
Art Unit: 2800  
Wednesday, September 10, 2008

Patent Number: 6320214

From: DIANE JACKSON  
Location: EIC2800  
JEF-4B68  
Phone: (571)272-3260

diane.jackson@uspto.gov

## Search Notes

Attached are litigation search results in Lexis Nexis, and CourtLink and Questel-Orbit.

No Litigation was found for Patent Number 6320214.

If you have any questions, please feel free to contact me.

Thanks,

Diane

SEP 9 2008

Page 1 of 1

Jackson, Diane

271810

From: Dees, Jose

Sent: Tuesday, September 09, 2008 2:52 PM

To: STIC-EIC2800

Subject: Reissue 10/611307

Please do a litigation search for the patent 6,320,214.

Thanks, J Dees

*Jose' G. Dees*

Jose' G. Dees

T-QAS, TC 2800

571-272-1569

9/9/2008

Selected file: PLUSPAT

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standardization  
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& JPB  
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Last update of file: 2008/09/05 (YYYY/MM/DD) 2008-36/UP (last update)

Search statement 1

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nbr /pn us6320214

PLUSPAT1 US6320210	1
PLUSPAT2 US6320211	1
PLUSPAT3 US6320212	1
PLUSPAT4 US6320213	1
PLUSPAT5 US6320214	1
PLUSPAT6 US6320215	1
PLUSPAT7 US6320216	1
PLUSPAT8 US6320217	1
PLUSPAT9 US6320218	1
PLUSPAT10US6320219	1
PLUSPAT11US632022	1
PLUSPAT12US6320220	1
PLUSPAT13US6320221	1
PLUSPAT14US6320222	1
PLUSPAT15US6320223	1

Some: numbers / Continue: Y / None: N

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Search 1 - 5 - 1

\*\* SS 1: Results 1

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Search statement 2

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prt full legalall max

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US6320214 (B1) Semiconductor device having a ferroelectric TFT and a dummy element

PN US6320214 B1 20011120 [US6320214]  
TI (B1) Semiconductor device having a ferroelectric TFT and a dummy element  
PA (B1) MATSUSHITA ELECTRIC IND CO LTD (US)  
PAO Matsushita Electric Industrial Company, Ltd., Osaka [JP]  
IN (B1) MATSUDA AKIHIRO (US); NAGANO YOSHIHISA (JP); UEMOTO YASUHIRO (JP)  
AP US20921498 19981211 [1998US-0209214]  
PR JP35443097 19971224 [1997JP-0354430]  
IC (B1) H01L-029/76  
ICAA H01L-021/8246 [2006-01 A - I R M EP]  
H01L-027/115 [2006-01 A - I R M EP]  
ICCA H01L-021/70 [2006 C - I R M EP]  
H01L-027/115 [2006 C - I R M EP]  
EC H01L-021/8246F6  
H01L-027/115F  
PCL ORIGINAL (O) : 257295000; CROSS-REFERENCE (X) : 257E21664  
257E27104 257213000 257296000  
DT Basic  
US5689126 [US5689126] 257306000  
CT -US5946563 [US5946563] 438183000  
-US6153476 [US6153476] 438275000  
STG (B1) U.S. Patent (no pre-grant pub.) after Jan. 2, 2001  
The present invention provides a semiconductor device including a semiconductor element and a dummy semiconductor element adjacent to the semiconductor element. When the semiconductor element is a capacitor element including a bottom electrode, a top electrode and a dielectric layer between the electrodes, a dummy capacitor element also has dummy electrodes and a dummy dielectric layer between the dummy electrodes. The dummy electrode is located so that a space between the top electrode of the capacitor element and the dummy top electrode is in a predetermined range (e.g. 0.3  $\mu\text{m}$  to 14  $\mu\text{m}$ ). The dummy capacitor element prevents the capacitor dielectric layer from degrading since the collision of the etching ions with the capacitor dielectric layer in a dry etching process is suppressed.  
UP 2001-48

☐ 1 / 1 LGST - Legal  
Status - ©EPO



US6320214 20010717 US/ASA [NMC]ASSIGNMENTOWNER: MATUSHITA  
ELECTRIC INDUSTRIAL CO., LT...  
PN US6320214 B1 20011120 [US6320214](B1) Granted patent as first publication  
AP US20921498 19981211 [1998US-0209214]  
19981211 US-API [POS; EXM]  
FILING DETAILS  
PUB US20921498 19981211 [1998US-0209214]  
20011120 US-B1 [POS; PIF]  
Granted patent as first publication  
US6320214 B1 20011120 [US6320214]  
20010717 US/AS-A [NMC]  
ASSIGNMENT  
OWNER: MATUSHITA ELECTRIC INDUSTRIAL CO., LTD. 1006-BANCH;  
EFFECTIVE DATE: 20010404  
MERGER;ASSIGNOR:MATSUSHITA ELECTRONICS CORPORATION  
/AR;REEL/FRAAME:011987/0526

20010717 US/AS-A [NMC]  
ACTASSIGNMENT  
OWNER: MATUSHITA ELECTRIC INDUSTRIAL CO., LTD. 1006-BANCH;  
EFFECTIVE DATE: 20010404  
MERGER;ASSIGNOR:MATSUSHITA ELECTRONICS  
CORPORATION;REEL/FRAAME:011987/0526

20050906 US/RF-A [OPP]  
REISSUE APPLICATION FILED  
EFFECTIVE DATE: 20030701  
LEG OPP  
Alive  
UP 2006-03

☐ 1 / 1 CRXX - US Claims  
Reassignments - ©CLAIMS/RRX

US6320214 20030710 REISSUE REQUESTEDISSUE DATE OF O.G.:  
20050906REISSUE REQUEST NUMBER...  
AN 3608091

PN 6,320,214 A 20011120 [US6320214]  
PA Matsushita Electric Industrial Co Ltd JP  
PT E (Electrical)  
20030710 REISSUE REQUESTED  
ISSUE DATE OF O.G.: 20050906  
REISSUE REQUEST NUMBER: 10/611307  
ACT EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2826

Reissue Patent Number:

UP 2005-36  
UACT 2005-09-06

Search statement 2  
Selected file: FAMPAT

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members  
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Coverage: 78 patenting authorities; start dates vary from 1800 forward  
Abstracts/Titles in English , French & German. Increased  
standardization  
of Assignee/Inventor names. Added cited ref's BE , CH , NL , TR , AU ,  
& JPB  
For more details , see the FamPat Fact Sheet and Questel website  
Last update of file: 2008/09/05 (YYYY/MM/DD) 2008-36/UP (last update)

Search statement 1

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nbr /pn us6320214

FAMPAT1 US6320210	1
FAMPAT2 US6320211	1
FAMPAT3 US6320212	1
FAMPAT4 US6320213	1
FAMPAT5 US6320214	1
FAMPAT6 US6320215	1
FAMPAT7 US6320216	1
FAMPAT8 US6320217	1
FAMPAT9 US6320218	1
FAMPAT10US6320219	1
FAMPAT11US632022	1
FAMPAT12US6320220	1
FAMPAT13US6320221	1
FAMPAT14US6320222	1

FAMPAT15US6320223

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Search 1 - 5 - 1

\*\* SS 1: Results 1

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 1 / 1 FAMPAT - Patent  
Families - ©Questel



JP11251554 Semiconductor device having a ferroelectric TFT and a dummy element  
FAN 20042790859120

PN JP11251554 A 19990917 [JP11251554]  
US6320214 B1 20011120 [US6320214]

TI Semiconductor device having a ferroelectric TFT and a dummy element  
PA MATSUSHITA ELECTRIC IND CO LTD  
MATSUSHITA ELECTRONICS CORP

PA0 Matsushita Electric Industrial Company, Ltd., Osaka [JP]  
IN MATSUDA AKIHIRO; NAGANO YOSHIHISA; UEMOTO YASUHIRO

AP 1998US-0209214 19981211  
1998JP-0365265 19981222

PR 1997JP-0354430 19971224  
1998JP-0365265 19981222

H01L-021/70  
H01L-021/8242  
H01L-021/8246

IC H01L-027/105  
H01L-027/108  
H01L-027/115  
H01L-029/76

ICAA H01L-021/8246 [2006-01 A - I R M EP]  
H01L-027/115 [2006-01 A - I R M EP]

ICCA H01L-021/70 [2006 C - I R M EP]  
H01L-027/115 [2006 C - I R M EP]

EC H01L-021/8246F6  
H01L-027/115F

PCL ORIGINAL (O) : 257295000; CROSS-REFERENCE (X) : 257E21664  
257E27104 257213000 257296000

FI H01L27/10 621Z; H01L27/10 651; H01L27/10 444B  
5F083 AD21; 5F083 AD48; 5F083 AD56; 5F083 FR02; 5F083 GA27;

FTM 5F083 JA12; 5F083 JA14; 5F083 JA15; 5F083 JA17; 5F083 JA38; 5F083  
JA43; 5F083 MA06; 5F083 MA17; 5F083 PR03; 5F083 ZA28  
(US6320214)

CT US5689126 [US5689126] 257306000  
-US5946563 [US5946563] 438183000  
-US6153476 [US6153476] 438275000  
(JP11251554)

CT (A)  
[19] Citation as reason for refusal of an application  
- JP (A) 1997321248 [JP09321248]  
(US6320214)  
The present invention provides a semiconductor device including a  
semiconductor element and a dummy semiconductor element adjacent to the  
semiconductor element. When the semiconductor element is a capacitor  
element including a bottom electrode, a top electrode and a dielectric layer  
between the electrodes, a dummy capacitor element also has dummy  
electrodes and a dummy dielectric layer between the dummy electrodes. The  
dummy electrode is located so that a space between the top electrode of the  
capacitor element and the dummy top electrode is in a predetermined range  
(e.g. 0.3  $\mu\text{m}$  to 14  $\mu\text{m}$ ). The dummy capacitor element prevents the  
capacitor dielectric layer from degrading since the collision of the etching  
ions with the capacitor dielectric layer in a dry etching process is suppressed.  
(US6320214)

AB The present invention provides a semiconductor device including a  
semiconductor element and a dummy semiconductor element adjacent to the  
semiconductor element.

OBJ The present invention relates to a semiconductor device.  
More particularly the present invention relates to a semiconductor device  
including a semiconductor element such as a capacitor and a transistor in  
which a dielectric with high dielectric constant or a ferroelectric is used.  
(US6320214)  
Therefore, the collision with the etching ions proceeding diagonally is  
suppressed to prevent the semiconductor element from degrading in  
electrical characteristics.


ADB These and other advantages of the present invention will become apparent  
those skilled in the art upon reading and understanding the following detailed  
description.  
According to the present invention, the surface of the dielectric film exposed  
to the etching ions is limited by the dummy element, which can reduce the  
electrical charges on the dielectric film.  
(US6320214)

ICLM 1. A semiconductor device comprising:  
a substrate,  
a semiconductor element on the substrate, the semiconductor element



including a first dielectric layer and an electrode on the first dielectric layer,  
and  
a dummy semiconductor element on the substrate, the dummy  
semiconductor element including a second dielectric layer and a dummy  
electrode on the second dielectric layer,  
wherein the dummy semiconductor element is located so that a space  
between the electrode and the dummy electrode is in a predetermined range,  
and the semiconductor device is a transistor in which the electrode works as  
a gate electrode of the transistor; and said first dielectric layer is composed  
of the material selected from a dielectric material having a dielectric constant  
of 100 or more and a ferroelectric material.

UP 2000-08

☐ 1 / 1 LGST - Legal   
Status - ©EPO

US6320214 20010717 US/ASA [NMC]ASSIGNMENTOWNER: MATUSHITA  
ELECTRIC INDUSTRIAL CO., LT...

PN US6320214 B1 20011120 [US6320214](B1) Granted patent as first publication

AP US20921498 19981211 [1998US-0209214]

19981211 US-API [POS; EXM]

FILING DETAILS

PUB US20921498 19981211 [1998US-0209214]

20011120 US-B1 [POS; PIF]

Granted patent as first publication

US6320214 B1 20011120 [US6320214]

20010717 US/AS-A [NMC]

ASSIGNMENT

OWNER: MATUSHITA ELECTRIC INDUSTRIAL CO., LTD. 1006-BANCH;

EFFECTIVE DATE: 20010404

MERGER;ASSIGNOR:MATSUSHITA ELECTRONICS CORPORATION

/AR;REEL/FRAME:011987/0526

20010717 US/AS-A [NMC]

ACTASSIGNMENT

OWNER: MATUSHITA ELECTRIC INDUSTRIAL CO., LTD. 1006-BANCH;

EFFECTIVE DATE: 20010404

MERGER;ASSIGNOR:MATSUSHITA ELECTRONICS

CORPORATION;REEL/FRAME:011987/0526

20050906 US/RF-A [OPP]

REISSUE APPLICATION FILED

EFFECTIVE DATE: 20030701

LEG OPP  
Alive  
UP 2006-03

☐ 1 / 1 CRXX - US Claims  
Reassignments - ©CLAIMS/RRX

US6320214 20030710 REISSUE REQUESTEDISSUE DATE OF O.G.:  
20050906REISSUE REQUEST NUMBER...  
AN 3608091  
PN 6,320,214 A 20011120 [US6320214]  
PA Matsushita Electric Industrial Co Ltd JP  
PT E (Electrical)  
20030710 REISSUE REQUESTED  
ISSUE DATE OF O.G.: 20050906  
REISSUE REQUEST NUMBER: 10/611307  
ACT EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2826

Reissue Patent Number:

UP 2005-36  
UACT 2005-09-06

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Patent Search 6320214 9/10/2008

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Source: Combined Source Set 10 - Utility, Design and Plant Patents

Terms: patno=6320214 ([Edit Search](#) | [Suggest Terms for My Search](#))

209214 (09) 6320214 November 20, 2001

## UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6320214

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November 20, 2001

Semiconductor device having a ferroelectric TFT and a dummy element

**REISSUE:** July 1, 2003 - Reissue Application filed Ex. Gp.: 2826; Re. S.N. 10/611,307 (O.G. September 6, 2005)**INVENTOR:** Matsuda, Akihiro - Colorado Springs, Colorado, United States (US); Nagano, Yoshihisa - Suita, Japan (JP); Uemoto, Yasuhiro - Otsu, Japan (JP)**APPL-NO:** 209214 (09)**FILED-DATE:** December 11, 1998**GRANTED-DATE:** November 20, 2001**PRIORITY:** December 24, 1997 - 9354430, Japan (JP)**ASSIGNEE-PRE-ISSUE:** April 7, 1999 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., MATSUSHITA ELECTRONICS CORPORATION 1-1, SAIWAI-CHO, TAKATSUKI-SHIOSAKA 569-1193, (1), Reel and Frame Number: 009878/0036

July 17, 2001 - MERGER (SEE DOCUMENT FOR DETAILS)., MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. 1006-BANCHIOAZA-KADOMA, KADOMA-SHI OSAKA 571-8501, (1), Reel and Frame Number: 011987/0526

**ASSIGNEE-AT-ISSUE:** Matsushita Electric Industrial Co., Ltd., Osaka, Japan (JP), Foreign company or corporation (03)**LEGAL-REP:** Merchant & Gould PC**PUB-TYPE:** November 20, 2001 - Utility Patent having no previously published pre-grant publication (B1)**PUB-COUNTRY:** United States (US)**US-MAIN-CL:** 257#295**US-ADDL-CL:** 257#213, 257#296, 257#E21.664, 257#E27.104**CL:** 257**SEARCH-FLD:** 257#532, 257#213, 257#295, 257#296-310

**Combined Source Set 14** - Intellectual Property Cases, Administrative Decisions & Regulations; Federal Register and CFR - Titles 19 and 37; Federal Register Documents Relating To Patent Issues; U.S. Patent & Trademark Office Decisions, Combined; Intellectual Property Law Review Articles, Combined; Patent, Trademark & Copyright Periodicals, Combined; Intellectual Property Law, Combined

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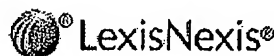
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*Laos Quest Economics Database* *World of Information Asia & Pacific Review* *World of Information* July 30,  
2007 Monday

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Quest Economics Database

World of Information Asia &amp; Pacific Review World of Information

July 30, 2007 Monday

**LENGTH:** 241 words**HEADLINE:** Laos**BODY:**

Agriculture contributes around 47 per cent of GDP and employs over 80 per cent of the workforce.

Rice, the main crop, is cultivated in irrigated lowland paddies and on drier hill farms.

Other crops include maize, sweet potatoes, cassava, pulses, groundnuts, fruit, vegetables, sugar cane, coffee, tobacco and cotton. Livestock raised includes cattle, buffaloes, pigs, goats and poultry.

Crop production in 2005 included: 2,560,000 tonnes (t) cereals in total, 2,350,000t rice, 210,000t maize, 60,000t cassava, 248,000t sweet potatoes, 48,000t bananas, 16,500t pulses, 70,500t citrus fruit, 9,632t oilcrops, 34,500t tobacco, 23,400t green coffee, 230,000t sugar cane, 350t tea, 7,000t seed cotton, 2,333t cotton lint, 206,100t fruit in total, 774,600t vegetables in total. Livestock production included: 89,291t meat in total, 22,500t beef, 18,623t buffalo meat, 28,000t pig meat, 518t goat meat, 19,650t poultry, 12,805t eggs, 1,973t cattle hides.

Exports of forest materials in 2004 were US\$62.7 million, while imports amounted to US\$1.7 million.

Production in 2004 included **6,320,214** cubic metres (cum) roundwood, 392,000cum industrial roundwood, 182,000cum sawnwood, 13,000cum wood-based panels, 260,000cum sawlogs and veneers, 5,928,214 cum wood fuel; 18,486t charcoal.

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6320214 or 6,320,214

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














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